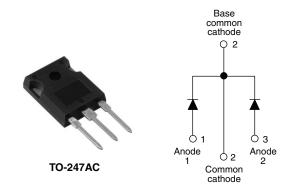
Vishay High Power Products

Schottky Rectifier, 2 x 20 A



2 x 20 A

40 V

60 mA at 100 °C

PRODUCT SUMMARY

I_{F(AV)}

 V_{R}

 I_{RM}

SHA

FEAT	URES
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- 150 °C T_J operation
- Center tap TO-247 package
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

The STPS40L40CWPbF center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	40	А		
V _{RRM}		40	V		
I _{FSM}	t _p = 5 μs sine	3500	А		
V _F	20 Apk, T_J = 125 °C (per leg)	0.43	V		
TJ		- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	STPS40L40CWPbF	UNITS	
Maximum DC reverse voltage	V _R	40	V	
Maximum working peak reverse voltage	V _{RWM}	40	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	$I_{F(AV)}$ 50 % duty cycle at T _C = 120 °C, rectangular waveform		40	
Maximum peak one cycle non-repetitive surge current per leg	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	3500	A
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse		430	
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 4 \text{ A}, L = 3.4 \text{ mH}$		27	mJ
Repetitive avalanche current per leg	I _{AR}			А	

* Pb containing terminations are not RoHS compliant, exemptions may apply



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	20 A	T _J = 25 °C	0.49	V
		40 A		0.59	
		20 A	• T _J = 125 °C	0.43	
		40 A		0.56	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V - Roted V	0.8	mA
See fig. 2		$T_J = 100 \ ^{\circ}C$	V _R = Rated V _R	60	ША
Maximum junction capacitance per leg	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		1850	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		7.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/µ		V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	i	T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case per leg		D	DC operation See fig. 4	1.25	
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	0.63	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased		
Annyovimete weight				6	g
Approximate weight				0.21	0Z.
Mounting torque	minimum		Non-lubricated threads	6 (5)	kgf ⋅ cm
	maximum			12 (10)	(lbf · in)
Marking device			Case style TO-247AC (JEDEC)	STPS40L40CW	



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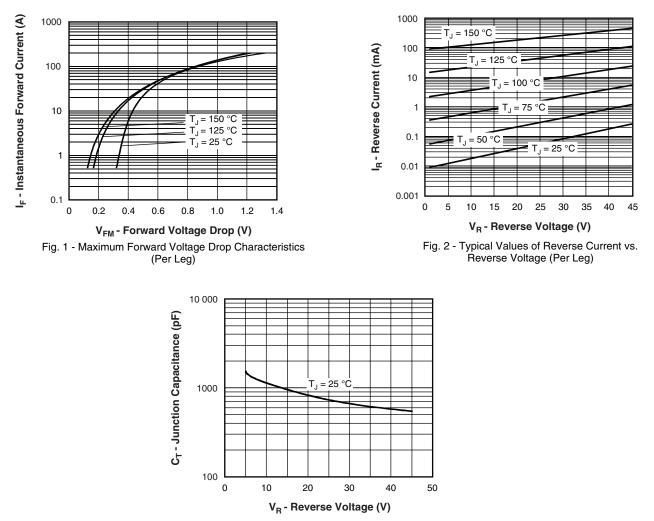
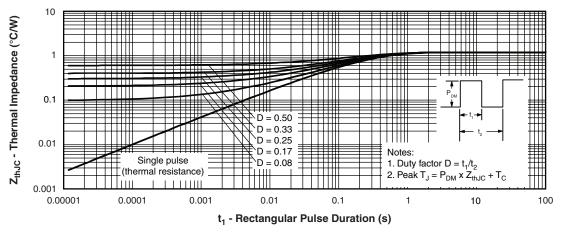


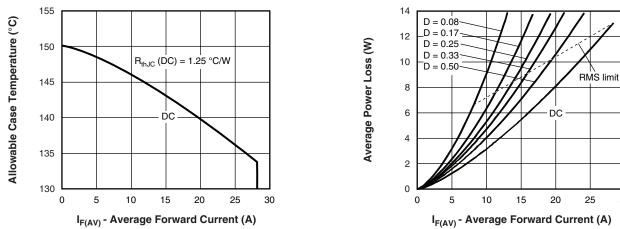
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

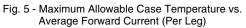


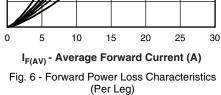


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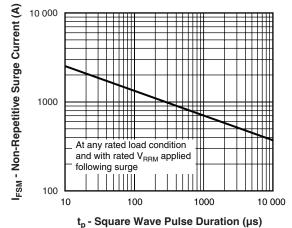


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

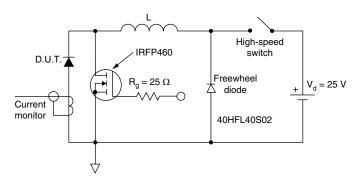
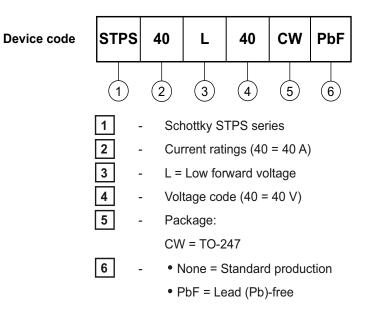


Fig. 8 - Unclamped Inductive Test Circuit



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ORDERING INFORMATION TABLE



Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95223			
Part marking information http://www.vishay.com/doc?95226			



Vishay

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